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10/050169
01/18/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10050169	01/18/2002	257		2814	
**APPLICANTS: Ikemasu Shinichiroh Okawa Narumi					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 08/876 908 06/16/1997 PAT 6,344,692					
** FOREIGN APPLICATIONS VERIFIED: JAPAN HEI 8-185424 07/18/1996					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		ATTORNEY DOCKET NO	
35 USC 119 conditions met		yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		970607B	
Verified and Acknowledged Examiner's initials					
TITLE: Highly integrated and reliable data and its manufacture <i>semiconductor memory</i> <i>device having electrical connection by side contact</i>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		DRAWING	
		Sheets Draw. Figs. Draw. Print Fig.	
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
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